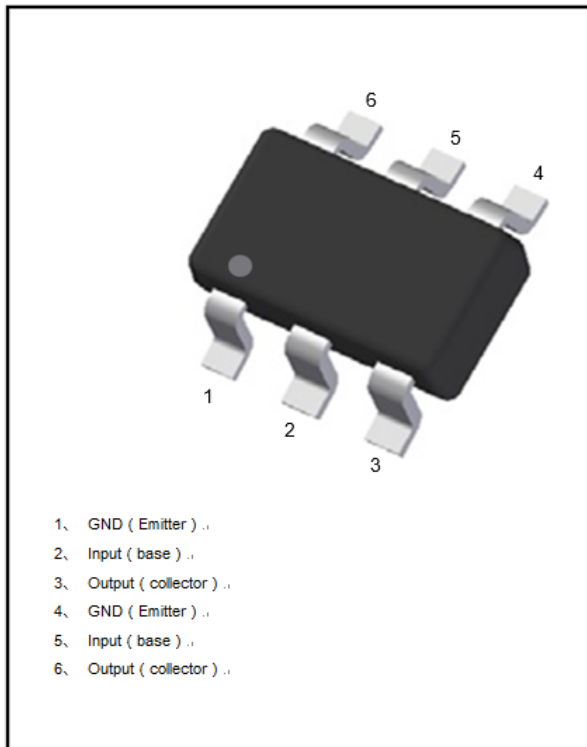


Dual NPN Digital Transistors (Built-in Resistors)



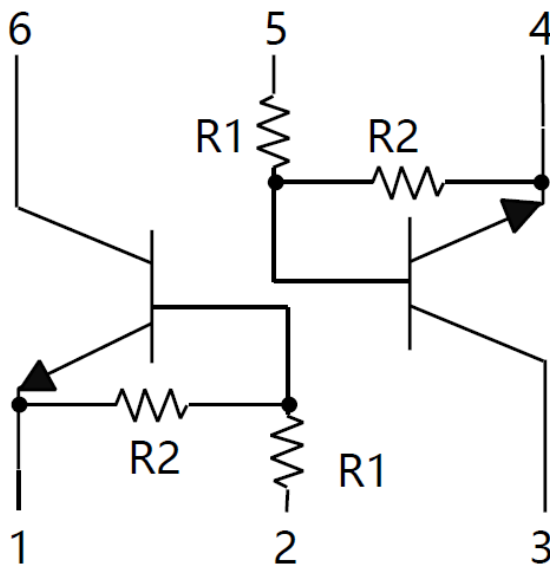
Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Mechanical Data

- **Package:** SOT-363S
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** H1

Equivalent circuit





UMH1NS

■Maximum Ratings (Ta=25°C Unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	VCC	V		50
Input Voltage	VIN	V		-10 to +40
Output Current	IO	mA		100
Power Dissipation	PD	mW		150
Junction Temperature (Single)	Tj	°C		150
Storage Temperature	TSTG	°C		-55 to +150

■Electrical Characteristics (Ta=25°C unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	VI(off)	V	VCC=5V, Ic=100uA	0.5	-	-
	VI(on)	V	V0=0.2V, Ic=5mA	-	-	3
Output voltage	VO(on)	V	Io /Ii =10mA/0.5 mA	-	-	0.3
Input current	II	mA	VI=5V	-	-	0.36
Output current	IO(off)	uA	VCC=50V, Vi=0	-	-	0.5
DC current gain	GI		Vo=5V, Io =5mA	56	-	-
Input resistance	R ₁	kΩ		15.4	22	28.6
Resistance ratio	R ₂ /R ₁			0.8	1	1.2
Transition frequency	fT	MHz	VCE=10V, IE=5mA, f=100MHz	-	250	-

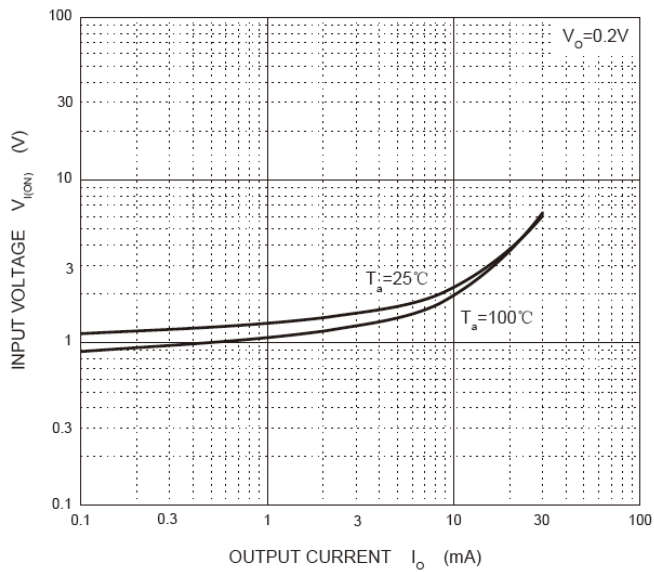
■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
UMH1NS	F2	Approximate 0.009g	3000	30000	120000	7" reel

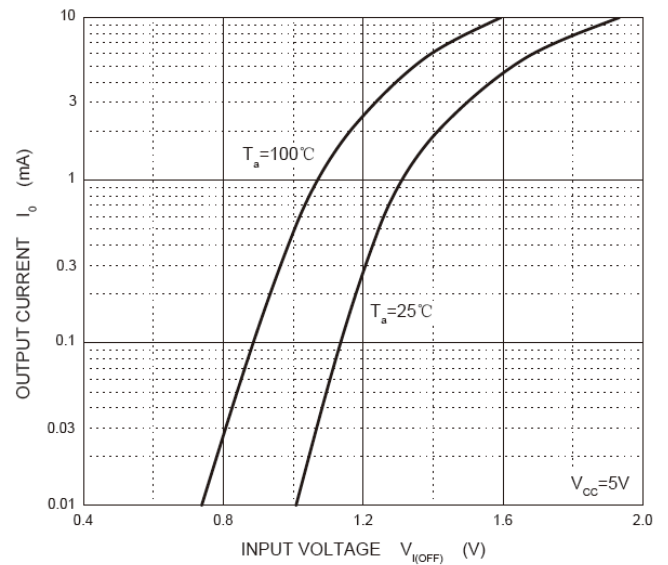


■ Characteristics (Typical)

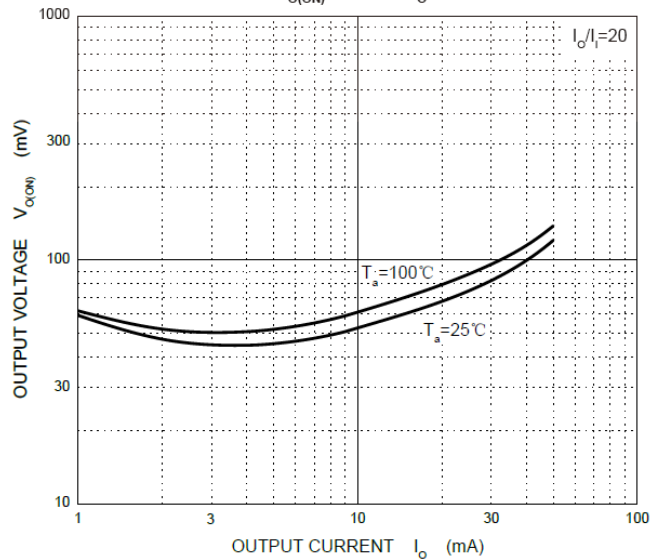
ON Characteristics



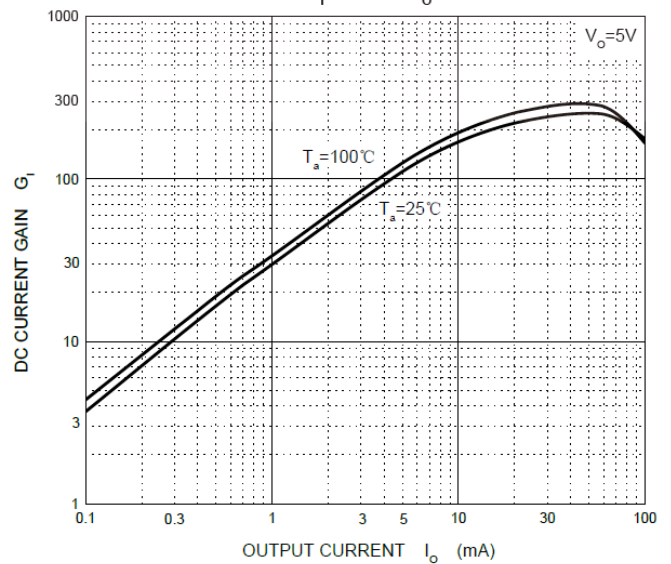
OFF Characteristics



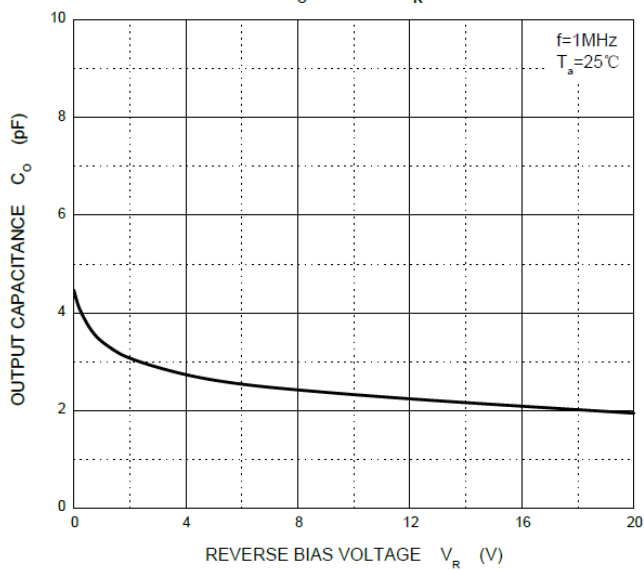
$V_{o(ON)}$ — I_o



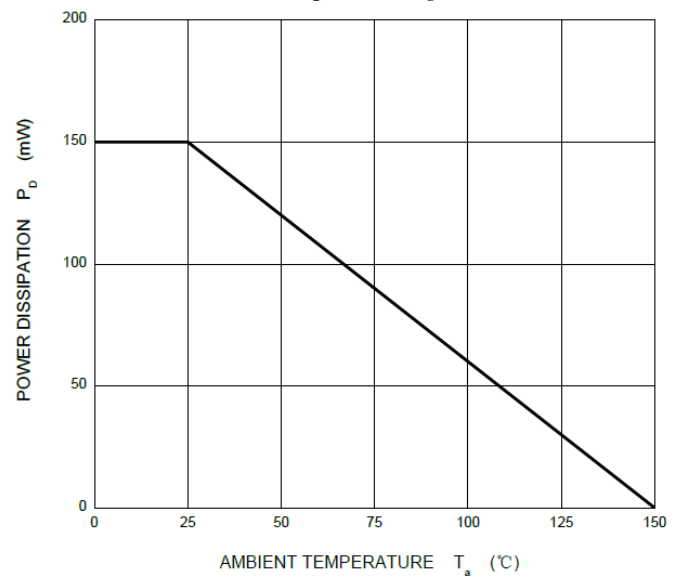
G_i — I_o



C_o — V_R



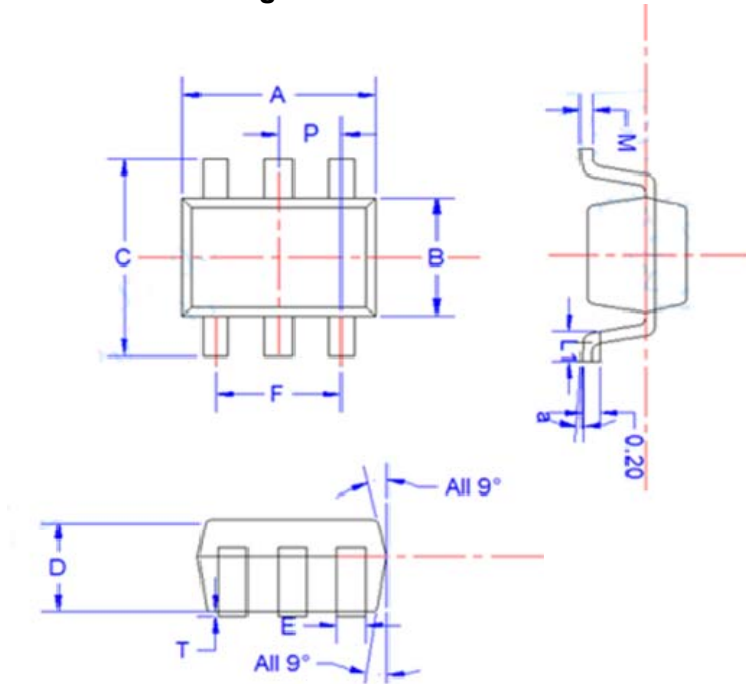
P_D — T_a





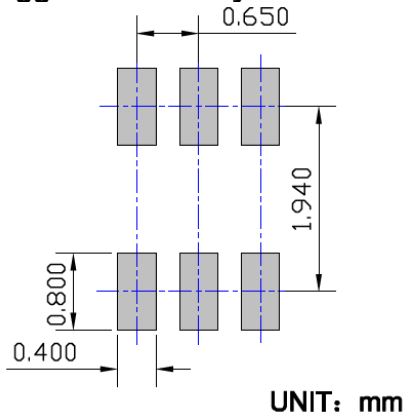
UMH1NS

■ SOT-363S Package Outline Dimensions



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
E	0.15	0.25	0.35
B	1.15	1.25	1.35
C	2.00	2.10	2.20
P	0.650BSC		
A	1.80	2.00	2.20
T	0.00	0.05	0.100
D	0.90	0.95	1.00
L1	0.20	0.30	0.40
a	4°±4°		
M	0.10	0.15	0.25

■ Suggested Pad Layout





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