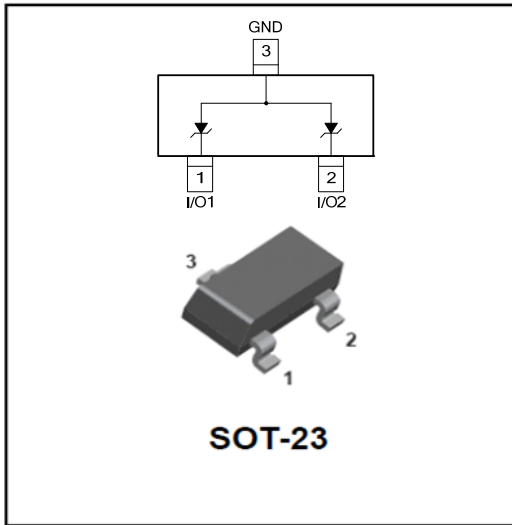


2-Line, Uni-directional, Ultra-low Capacitance, Transient Voltage Suppressor

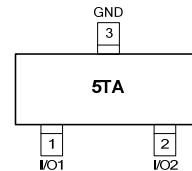


Features

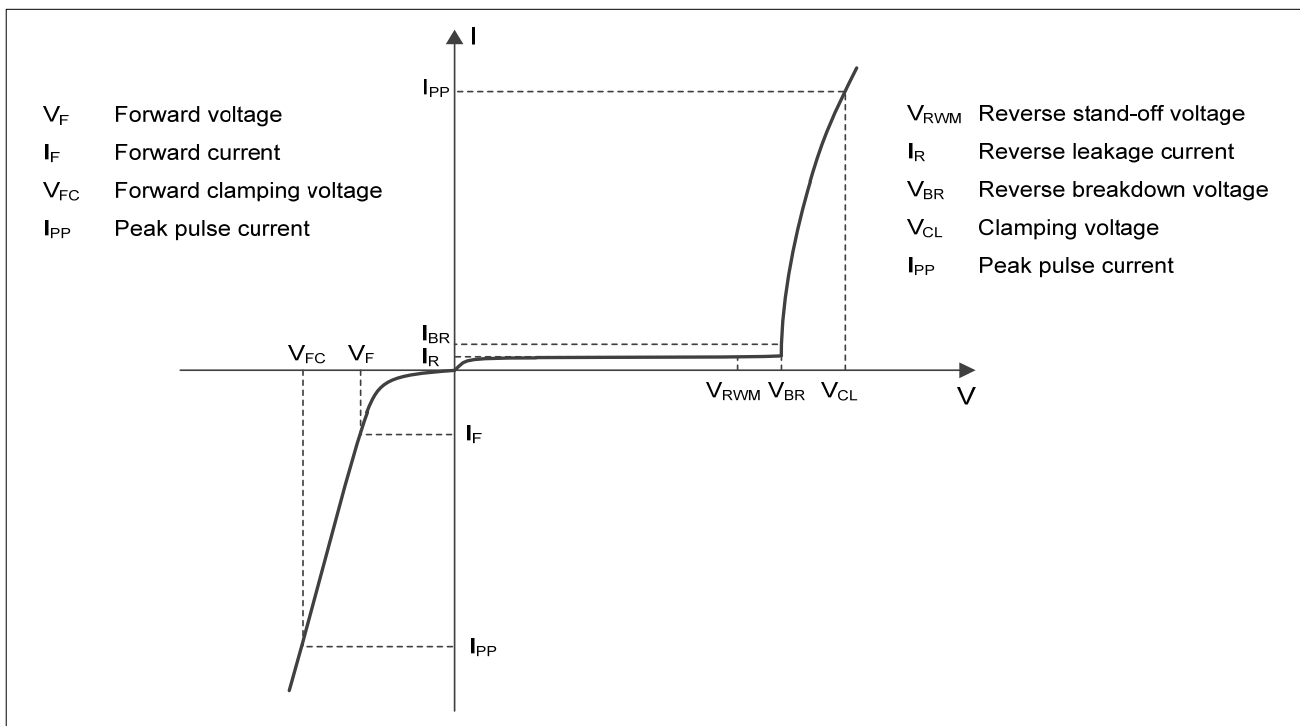
- Stand-off voltage: 5V Max
- Transient protection for each line according to IEC61000-4-2(ESD): $\pm 20\text{kV}$ (contact)
IEC61000-4-5(surge): 3.5A (8/20 μs)
- Ultra-low capacitance: $C_J = 0.4\text{pF}$ typ.
- Low leakage current: $I_R < 1\text{nA}$ typ.
- Low clamping voltage: $V_{CL} = 14.0\text{V}$ typ. @ $I_{PP} = 16\text{A}$ (TLP)
- Solid-state silicon technology
- Halogen-free

Mechanical Data

- **Package:** SOT-23
- **Terminals:** Tin plated leads, solderabl per J-STD-002 and JESD22-B102
- **Marking:**



■Definitions of electrical characteristics





ESDSL5V0E

■ Maximum Ratings

PARAMETER	SYMBOL	LIMITS	UNIT
Peak pulse power ($t_p = 8/20\mu s$)	P_{pk}	42	W
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	3.5	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 20	KV
ESD according to IEC61000-4-2 contact discharge		± 20	
Junction temperature	T_J	125	$^{\circ}C$
Operating temperature	T_{OP}	-40~85	$^{\circ}C$
Storage temperature	T_{STG}	-55~150	$^{\circ}C$

■ Electrical Characteristics ($T_a=25^{\circ}C$ Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	V_{RWM}	V				5
Reverse leakage current	I_R	nA	$V_{RWM} = 5V$		<1	100
Reverse breakdown voltage	$V_{(BR)}$	V	$I_T = 1mA$	7	8	9
Forward voltage	V_F	V	$I_T = 10mA$	0.6	0.9	1.2
Clamping voltage ¹⁾	V_{CL}	V	$I_{PP} = 16A, t_p = 100ns$		14	
Dynamic resistance ¹⁾	R_{DYN}	Ω			0.33	
Clamping voltage ²⁾	V_{CL}	V	$V_{ESD} = 8kV$		14	
Clamping voltage ³⁾	V_{CL}	V	$I_{PP} = 1A, t_p = 8/20\mu s$			10
		V	$I_{PP} = 3.5A, t_p = 8/20\mu s$			12
Junction capacitance	CJ	μF	$V_R = 0V, f = 1MHz$ Any I/O pin to GND		0.4	0.65
			$V_R = 0V, f = 1MHz$ Between any I/O pin		0.25	0.4

Notes:

- 1) TLP parameter: $Z_0 = 50\Omega, t_p = 100ns, t_r = 2ns$, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
- 2) Contact discharge mode, according to IEC61000-4-2.
- 3) Non-repetitive current pulse, according to IEC61000-4-5.

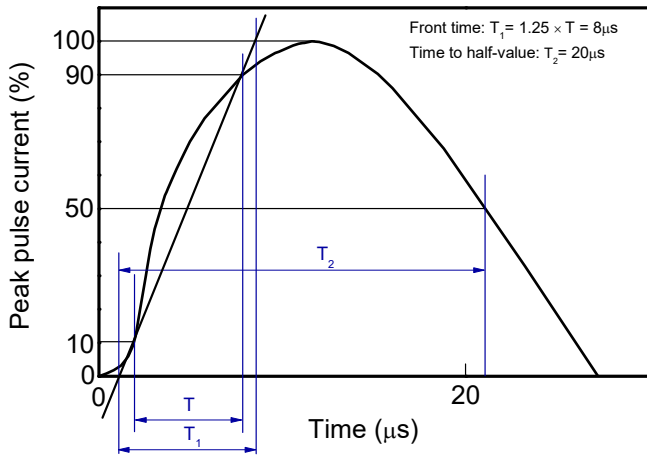
■ Ordering Information (Example)

PREFERRED P/N	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESDSL5V0E	3000	30000	120000	7 reel

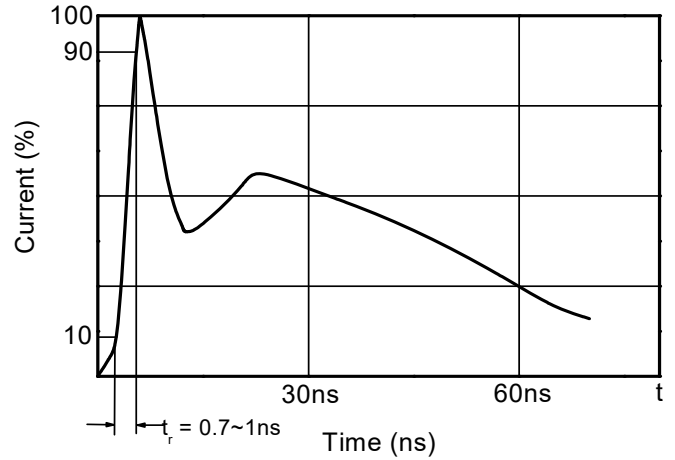


■ Characteristics (Typical)

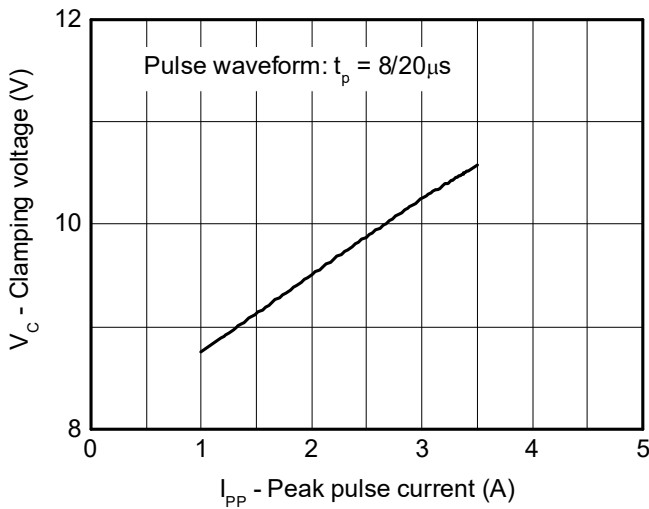
8/20 μ s waveform per IEC61000-4-5



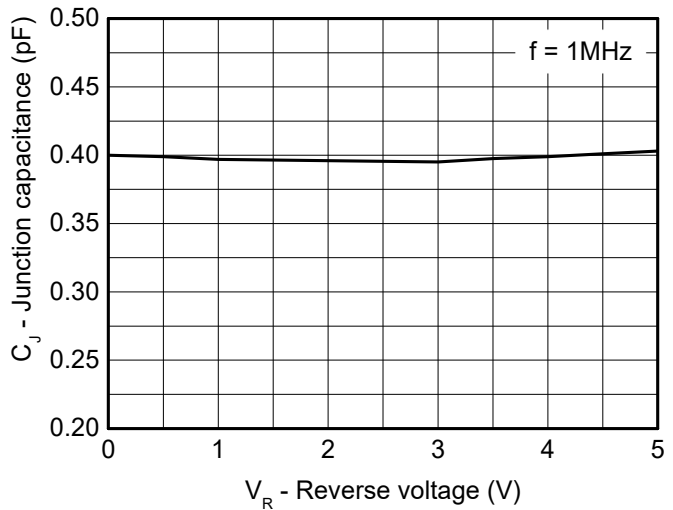
Contact discharge current waveform per IEC61000-4-2



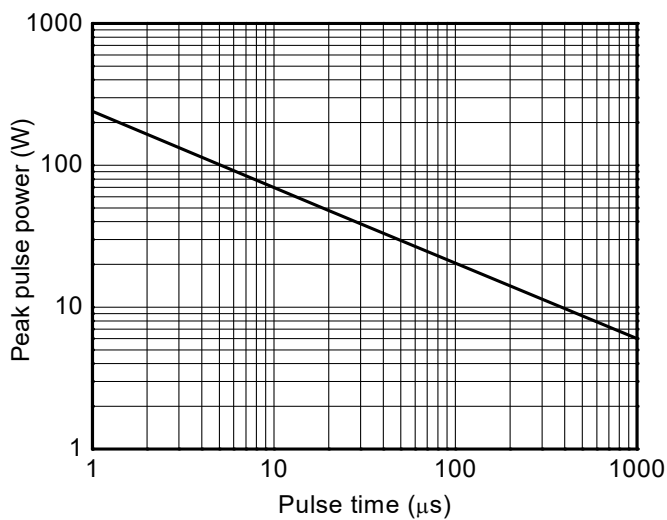
Clamping voltage vs. Peak pulse current



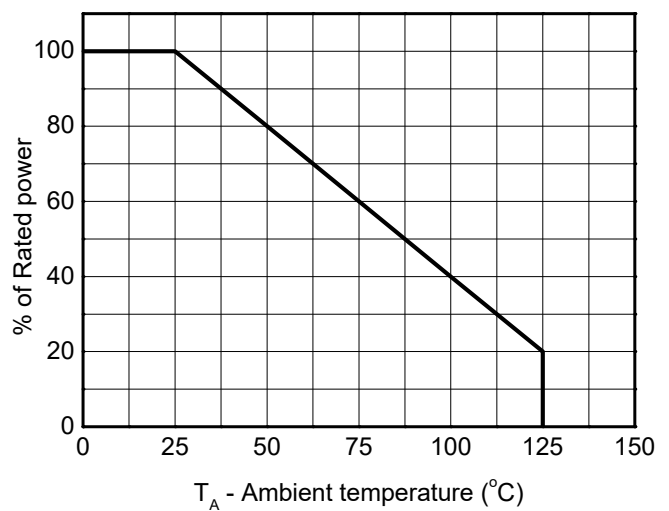
Capacitance vs. Reverse voltage



Non-repetitive peak pulse power vs. Pulse time



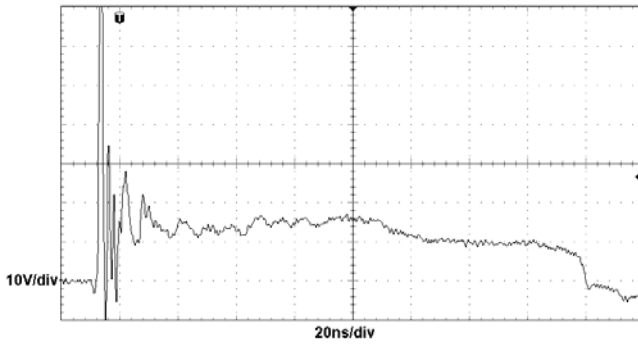
Power derating vs. Ambient temperature



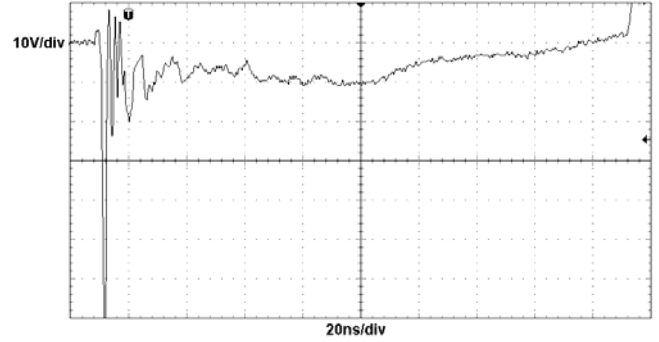


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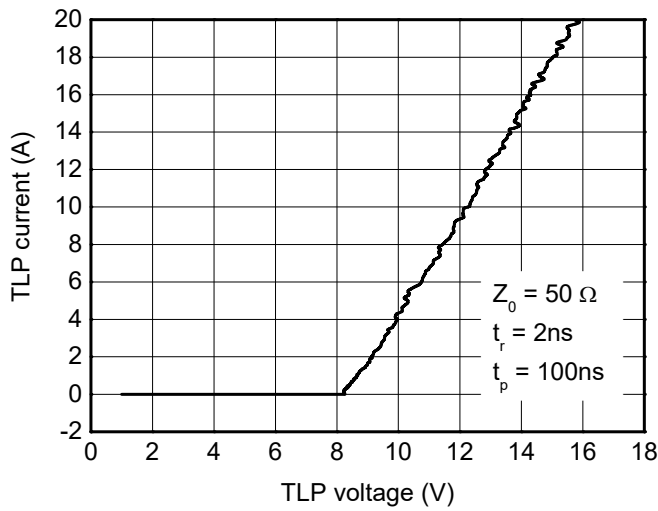
ESD clamping
(+8kV contact discharge per IEC61000-4-2)



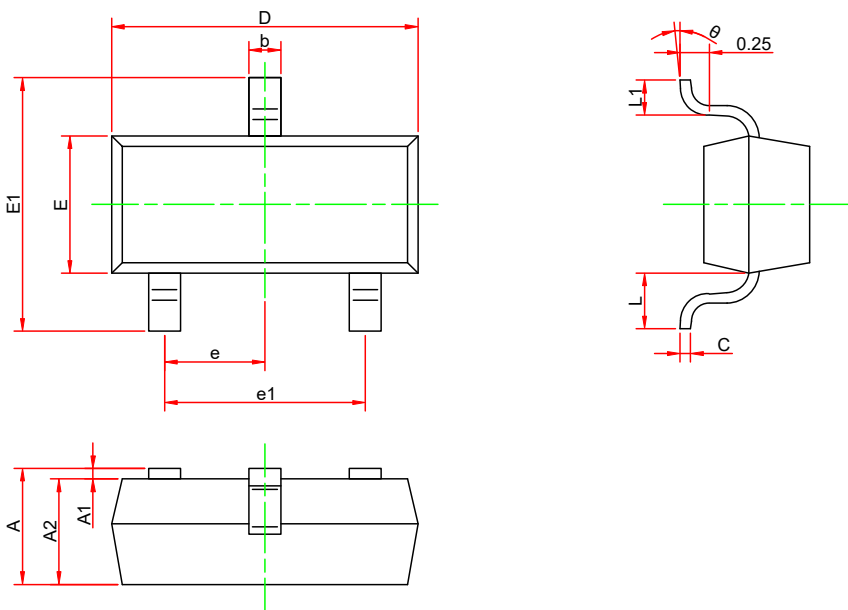
ESD clamping
(-8kV contact discharge per IEC61000-4-2)



TLP Measurement



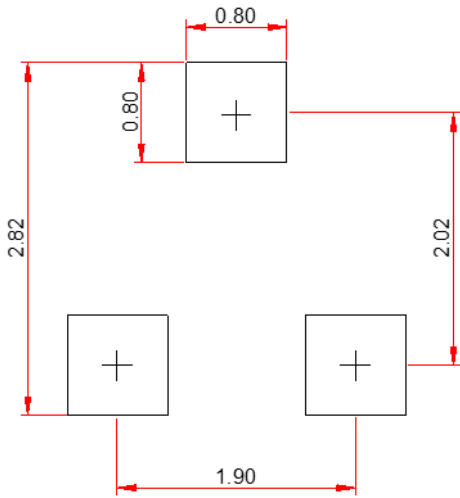
■ Outline Dimensions



Symbol	Dimensions in millimeters		
	Min.	Typ.	Max.
A	0.900	-	1.150
A1	0.000	-	0.100
A2	0.900	-	1.050
b	0.300	-	0.500
c	0.080	-	0.150
D	2.800	-	3.000
E	1.200	-	1.400
E1	2.250	-	2.550
e	0.950TYP		
e1	1.800	-	2.000
L	0.550REF		
L1	0.300	-	0.500
θ	0°	-	8°



■ Soldering Footprint



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.



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