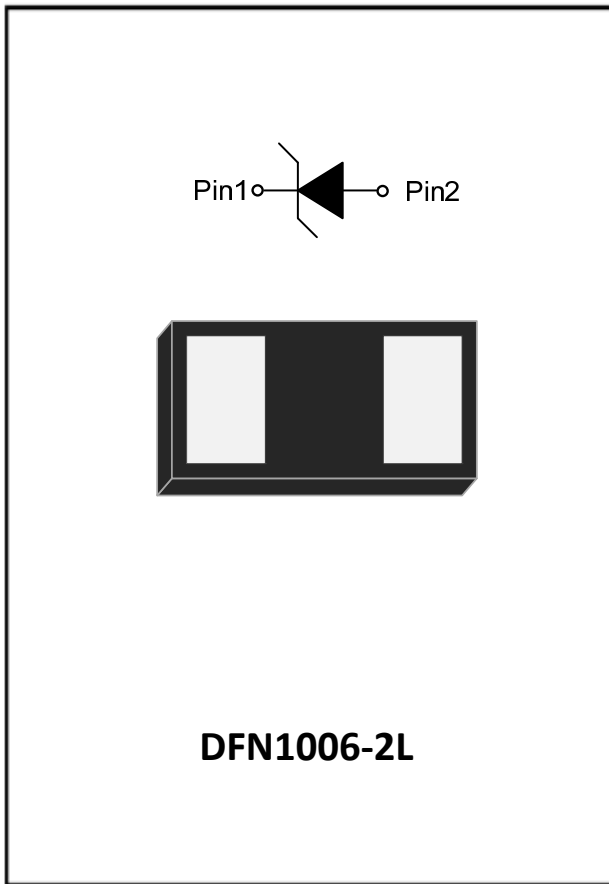


1-Line, Uni-directional , Transient Voltage Suppressor



Features

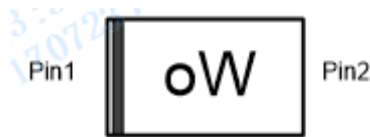
- Stand-off voltage: 24V Max
- Transient protection for each line according to
 - IEC61000-4-2(ESD): $\pm 30\text{kV}$ (contact)
 - IEC61000-4-4 (EFT): 40A (5/50ns)
 - IEC61000-4-5(surge): 22A (8/20 μs)
- Low leakage current
- Ultra-low capacitance: $C_J = 105\text{pF}$ typ
- Low clamping voltage:
 $V_{CL} = 32\text{V}$ typ. @ $I_{PP} = 16\text{A}$ (TLP)
- RoHS Compliant

Applications

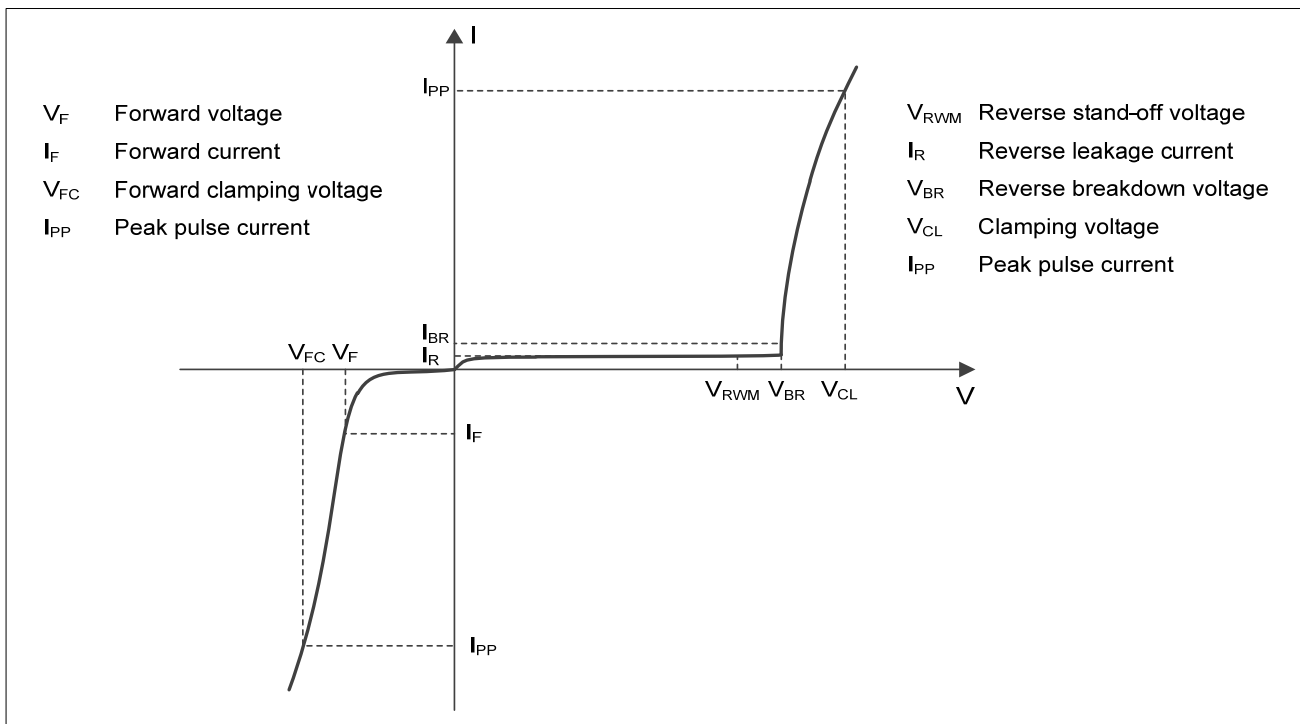
- Cellular handsets
- Tablets
- Laptops
- Other portable devices
- Network communication devices

Mechanical Data

- Package: DFN1006-2L
- Case Material: "Green" Molding Compound
- Moisture Sensitivity: Level 3 per J-STD-020
- Marking Information: See Below



■Definitions of electrical characteristics





ESD24VLA1

■Maximum Ratings

PARAMETER	SYMBOL	LIMITS	UNIT
Peak pulse power ($t_p = 8/20\mu s$)	P_{pk}	1078	W
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	9	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 30	kV
ESD according to IEC61000-4-2 contact discharge		± 30	
Junction temperature	T_J	-45~125	$^{\circ}C$
Storage temperature	T_{STG}	-55~150	$^{\circ}C$

■Electrical Characteristics ($T_a=25^{\circ}C$ Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	V_{RWM}	V				24
Reverse leakage current	I_R	μA	$V_{RWM} = 24V$			0.1
Reverse breakdown voltage	V_{BR}	V	$I_{BR} = 1mA$	25	28.8	
Clamping voltage ¹⁾	V_{CL}	V	$I_{PP} = 16A, t_p = 100ns$		32	
Dynamic resistance ¹⁾	R_{DYN}	Ω			0.22	
Clamping voltage ²⁾	V_{CL}	V	$V_{ESD} = 8kV$		34	
Clamping voltage ¹⁾	V_{CL}	V	$I_{PP} = 1A, t_p = 8/20\mu s$		28	32
		V	$I_{PP} = 22A, t_p = 8/20\mu s$		46.5	52
Junction capacitance	C_J	pF	$V_R = 0V, f = 1MHz$		105	125

Notes:

- 1) TLP parameter: $Z_0 = 50\Omega, t_p = 100ns, t_r = 2ns$, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.
- 2) Contact discharge mode, according to IEC61000-4-2.
- 3) Non-repetitive current pulse, according to IEC61000-4-5.

■Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(mg)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESD24VLA1	F1	Approximate 0.9	10000	100000	400000	7" reel



ESD24VLA1

■ Characteristics (Typical)

Fig.1 8/20 μ s waveform per IEC61000-4-5

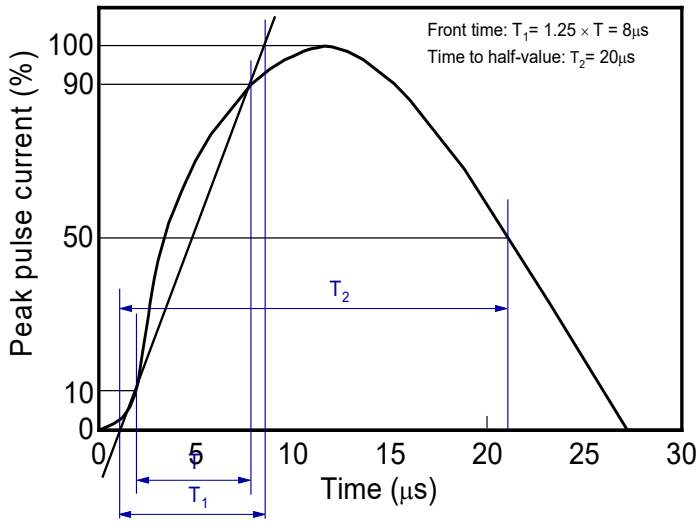


Fig.2 Contact discharge current waveform per IEC61000-4-2

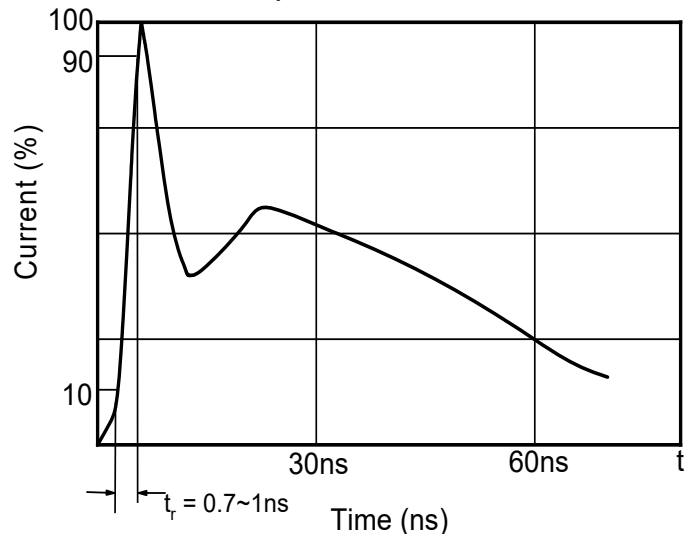


Fig.3 Clamping voltage vs. Peak pulse current

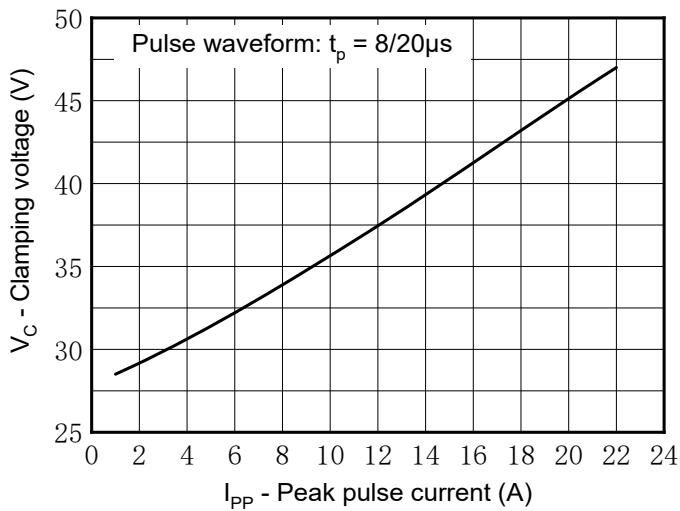


Fig.4 Capacitance vs. Reverse voltage

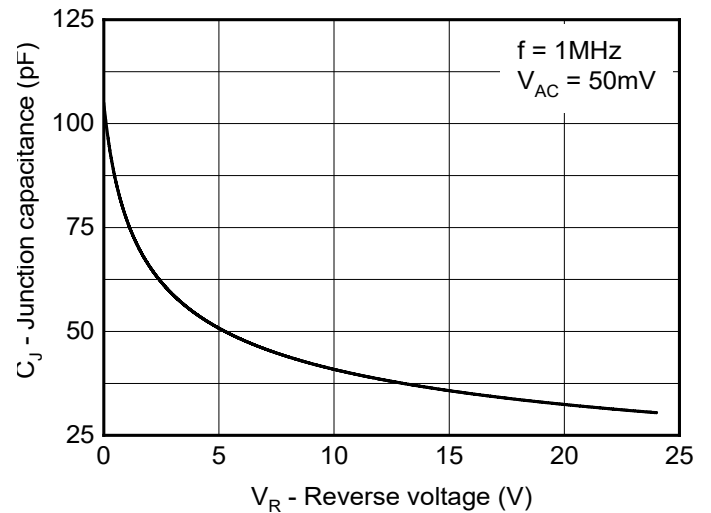


Fig.5 Non-repetitive peak pulse power vs. Pulse time

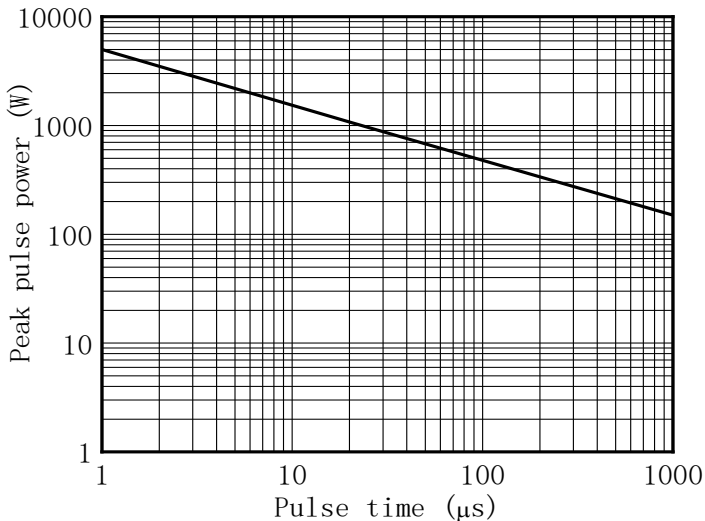
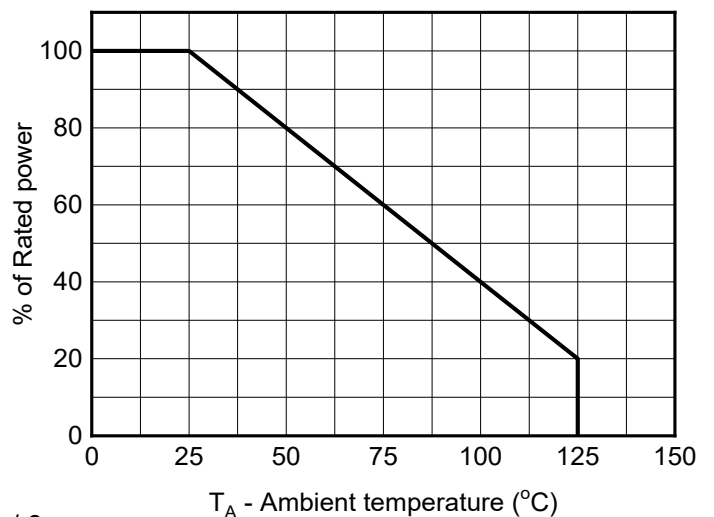


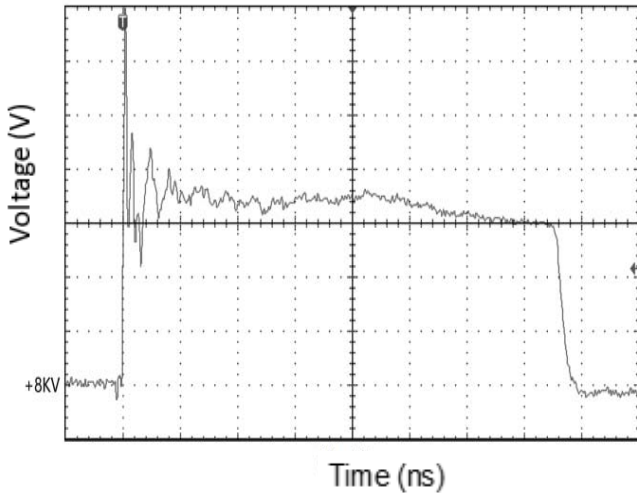
Fig.6 Power derating vs. Ambient temperature





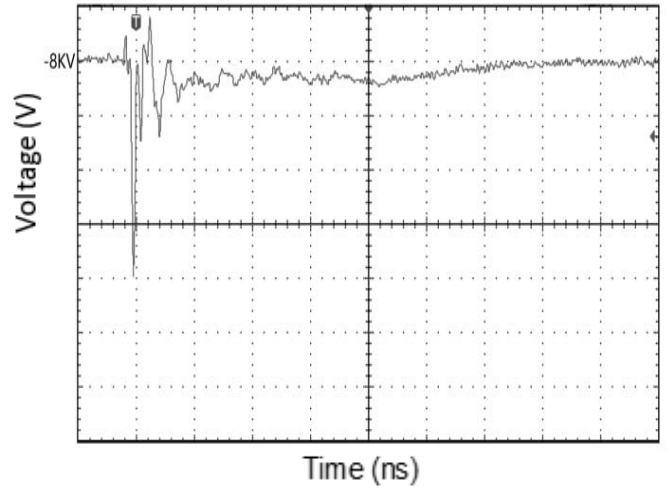
ESD24VLA1

Fig.7 ESD clamping



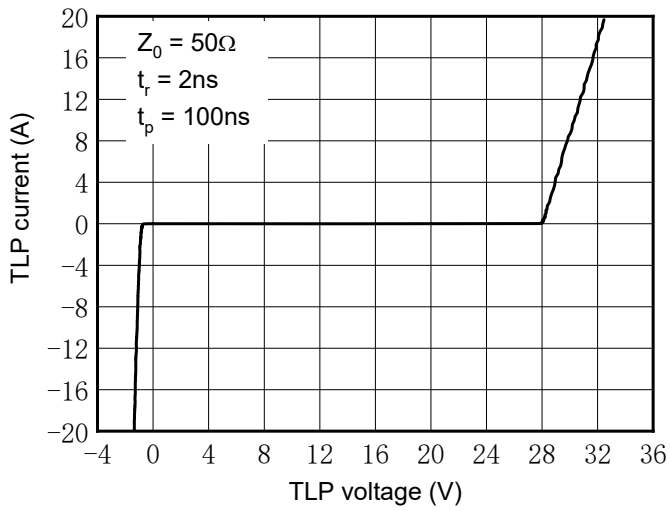
(+8kV contact discharge per IEC61000-4-2)

Fig.8 ESD clamping t

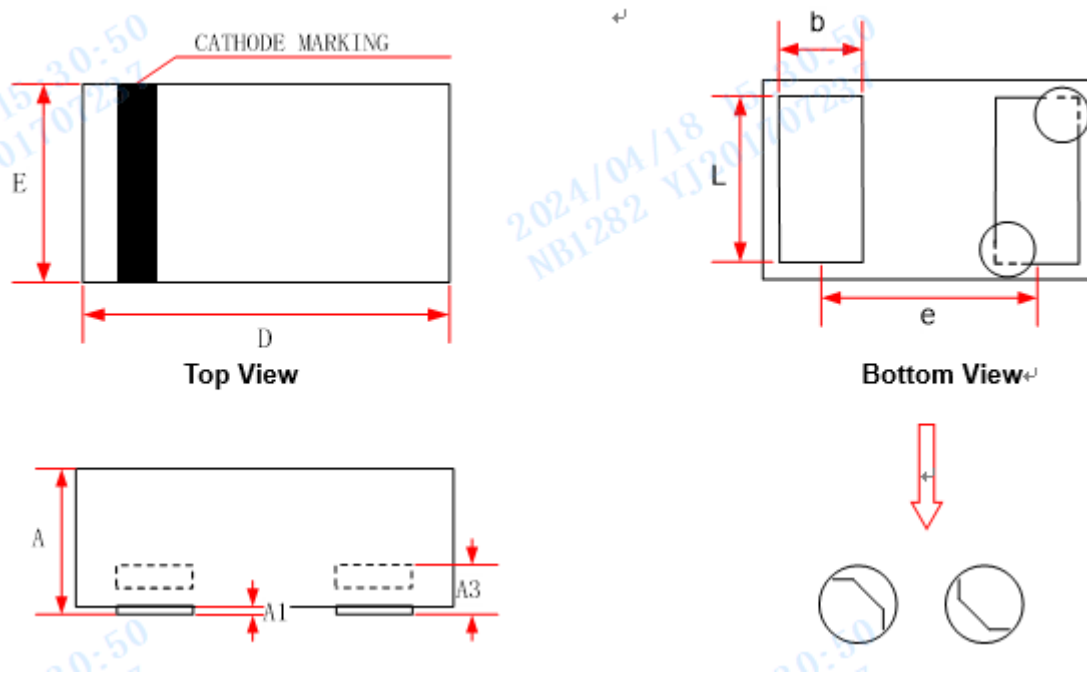


(-8kV contact discharge per IEC61000-4-2)

Fig.9 TLP Measurement

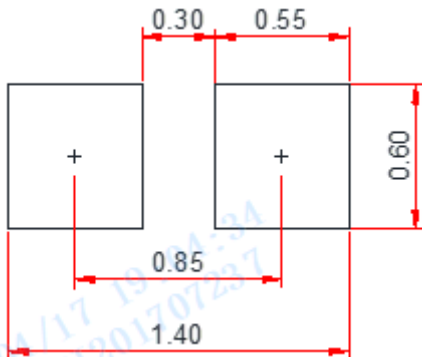


■ Outline Dimensions



Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.400	0.450	0.550
A1	0.000	0.020	0.050
A3	0.077	0.127	0.177
D	0.950	1.000	1.05
E	0.550	0.600	0.65
b	0.200	0.250	0.300
L	0.450	0.500	0.550
e	0.650 BSC		

■ Recommended PCB Layout



Unit:mm

Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met

Disclaimer



ESD24VLA1

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